

# JC-61 Liaison Report

Tim Wakeley, HP

# JC-61 Committee

<b>Officers:</b>	Noman Rangwala (AD)	Chair
	Tim Wakeley (HP)	Vice-Chair
	Patrick Yu (ALi)	Mktg. Task Group Chair
	Tim Wakeley (HP)	Tech, I&C TG Chair
	Jack Winters	Technical Editor

## 41 Member Companies

ATI Research	Agere	Agilent	ALi
AMD	Amkor	Analog Devices	ArrayComm
Atmel	Broadcom	Buffalo	Cirrus Logic
Cisco	EEMS Italia	Freescall	Fujitsu
Globespan	HP	IBM	Infineon
Intel	Intersil	Maxim	Molex
Motia	NEC	NewLogic	Nokia
Nvidia	Pericom	Philips	PMC Sierra
Renesas	RFMD	Sharp	Sony
STMicroelectronics	TI	Theta Micro	UTAC
Via Technologies			

# JC-61 BB-RF Interface

- BB-RF Interface standard published as

## JESD96

- Free for download at:
  - <http://www.jedec.org/download/search/JESD96.pdf>
- JESD96 Press Release
  - [http://www.jedec.org/Home/press/press\\_release/PR\\_JESD96.pdf](http://www.jedec.org/Home/press/press_release/PR_JESD96.pdf)

# JESD96 Rev 1

- JESD96 Rev 1 is out for ballot
  - Adding optional additional TX clock line

# JC-61 BB-RF I&C Standard

- 2 Companies presented 1<sup>st</sup> Showings
- Hope to have Baseline proposal to ballot by May 2005

# FCC NPRM on Partitioned Modules

- Committee understands FCC labs is moving forward with the NPRM
- Some companies are close to having reference designs for FCC labs to test

# JC-61 Next Meetings Information

- March 14, 8-Noon in Atlanta, GA
  - Hopefully same hotel as IEEE